



AP13 Rec'd PCT/PTO 14 DEC 2006 #
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79257

Katsuki KUSUNOKI

Appln. No.: 10/581,335

Group Art Unit: Not Yet Assigned

Confirmation No.: 6238

Examiner: Not Yet Assigned

Filed: June 2, 2006

For: METHOD FOR PRODUCTION OF SEMICONDUCTOR CHIP, AND
SEMICONDUCTOR CHIP

SUBMISSION OF EXECUTED DECLARATION

MAIL STOP PCT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In view of the application filed on June 2, 2006 without the appropriate executed documents, and since Applicant's attorney has not yet received the "Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US)," for the above application, Applicant submits herewith a copy of the Declaration (three (3) papers) for the above identified application properly executed by the inventors.

A check for the statutory fee of \$130.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

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Date: December 14, 2006

Respectfully submitted,

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